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January 13, 2004 -

To: Commissioner for Patents P.O.Box 1450

Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572

28 Davis Avenue

Poughkeepsie, N.Y. 12603

Subject:

| Serial No. 10/697,745 10/30/03 |

Wuping Liu et al.

SELF-PATTERNING OF PHOTO-ACTIVE DIELECTRIC MATERIALS FOR INTER-CONNECT ISOLATION

## INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation In An Application.

The following Patents and/or Publications are submitted to comply with the duty of disclosure under CFR 1.97-1.99 and 37 CFR 1.56.

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 77, 2004.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

CS-03-025

The following three U.S. Patents describe the use of photo-active dielectrics in printed circuit board manufacture:

- 1) U.S. Patent 6,506,979 to Shelnut et al., "Sequential Build Circuit Board."
- 2) U.S. Patent 5,334,488 to Shipley, Jr., "Method for Manufacture of Multilayer Circuit Board."
- 3) U.S. Patent 6,521,328 to Lauffer et al., "Copper Etching Compositions and Products Derived Therefrom."
- U.S. Patent 6,349,456 to Dunn et al., "Method of Manufacturing Photodefined Integral Capacitor with Self-Aligned Dielectric and Electrodes," teaches forming a resistor using a photo-active dielectric (PAD).

Sincerely,

Stephen B. Ackerman,

Reg. No. 37761

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